

The University of Nottingham

SCHOOL OF COMPUTER SCIENCE

A LEVEL 3 MODULE, AUTUMN SEMESTER 2017–2018

COMPILERS

Time allowed TWO hours

Candidates may complete the front cover of their answer book and sign their desk card but must NOT write anything else until the start of the examination period is announced.

Answer ALL THREE questions

No calculators are permitted in this examination.

Dictionaries are not allowed with one exception. Those whose first language is not English may use a standard translation dictionary to translate between that language and English provided that neither language is the subject of this examination. Subject-specific translation directories are not permitted.

No electronic devices capable of storing and retrieving text, including electronic dictionaries, may be used.

DO NOT turn examination paper over until instructed to do so

ADDITIONAL MATERIAL: Appendix A, Appendix B

INFORMATION FOR INVIGILATORS: none

Question 1

(a) Explain and give examples of the following kinds of compile-time error:

- lexical error
- syntax error (context-free)
- contextual error

(6)

(b) Draw the *parse* (or *derivation*) tree for the following MiniTriangle fragment. The relevant grammar is given in Appendix A. Start from the production for “Command”.

```
if x[i] < 100 then
    putint(k)
else
    i := (-i) - 1
```

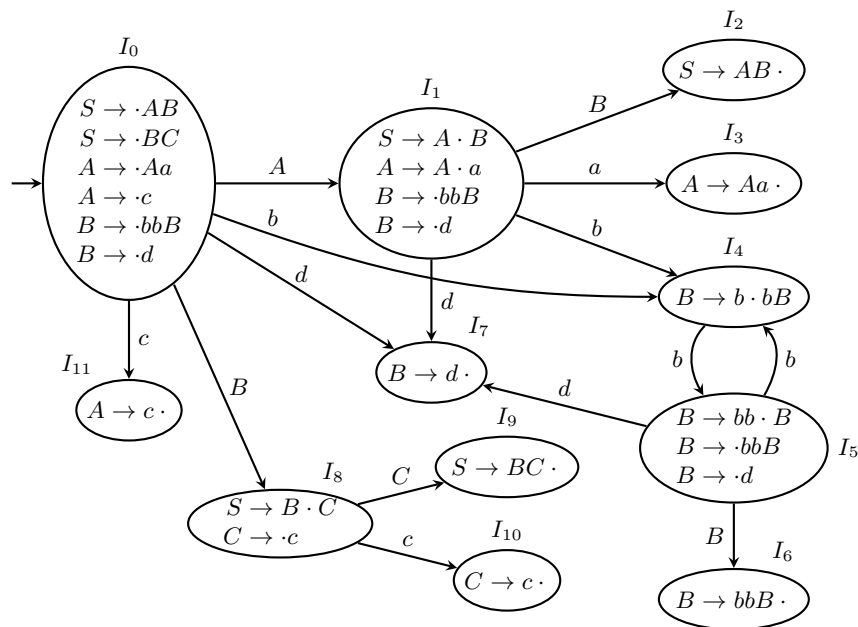
(9)

(c) Consider the following context-free grammar (CFG):

$$\begin{aligned} S &\rightarrow AB \mid BC \\ A &\rightarrow Aa \mid c \\ B &\rightarrow bbB \mid d \\ C &\rightarrow c \end{aligned}$$

S , A , B , and C are nonterminal symbols, S is the start symbol, and a , b , c , and d are terminal symbols.

The DFA below recognizes the viable prefixes for this CFG:



Show how an LR(0) shift-reduce parser parses the string $caabbbbd$ by completing the following table (copy it to your answer book; do *not* write on the examination paper):

State	Stack	Input	Move
I_0	ϵ	$caabbbbd$	Shift
I_{11}	c	$aabbbbd$	Reduce by $A \rightarrow c$
\vdots	\vdots	\vdots	\vdots
	S	ϵ	Done

(10)

Question 2

Consider the language given by the following abstract syntax:

$C \rightarrow$	skip $ C ; C$ $ x := E$ $ \text{if } E \text{ then } C \text{ else } C$ $ \text{while } E \text{ do } C$	<i>Commands:</i> <i>Do nothing</i> <i>Sequencing</i> <i>Assignment</i> <i>Conditional command</i> <i>while-loop</i>
$E \rightarrow$	n $ x$ $ E + E$ $ E = E$	<i>Expressions:</i> <i>Literal integer</i> <i>Variable</i> <i>Addition</i> <i>Comparison</i>

For this question, you will develop code generation functions for the above language, targeting the Triangle Abstract Machine (TAM). See appendix B for a specification of the TAM instructions. Assume conventional (imperative) semantics for the above language constructs, along with the following:

- x is the syntactic category of variable identifiers, ranging over the 26 names a, b, ..., z. They refer to 26 global variables stored at SB + 0 (a) to SB + 25 (z).
- The while-loop has the following semantics: the loop expression E is evaluated; if the result is true, the loop body C is executed next and then the process is repeated from the evaluation of the loop expression; otherwise execution continues after the loop.

The code generation functions should be specified through *code templates* in the style used in the lectures. Assume a function $addr(x)$ that returns the address (of the form [SB + d]) for a variable x . Further, you will have to consider generation of fresh labels. Assume a monadic-style operation $l \leftarrow fresh$ to bind a variable l to a distinct label that then can be used in jumps and as jump targets. For example:

$$\begin{aligned}
 \text{execute } \llbracket \text{if } E \text{ then } C_1 \text{ else } C_2 \rrbracket &= && l_1 \leftarrow \text{fresh} \\
 &&& \dots \\
 &&& \text{JUMP } l_1 \\
 &&& \dots \\
 &&& l_1: \dots
 \end{aligned}$$

- (a) Write a code generation function *evaluate* that generates TAM code for evaluating an expression. The first case should start like:

$$\text{evaluate } \llbracket n \rrbracket = \dots$$

(4)

- (b) Write a code generation function *execute* that generates TAM code for executing commands. It should handle the five forms of commands specified by the abstract syntax above. (12)
- (c) Now assume we wish to extend the language with the commands *break* and *continue* :

C	→		...		break		continue		Commands:
			...		break		continue		Terminate innermost loop
			...		break		continue		Continue with next loop iteration

The semantics is that *break* will terminate the innermost loop, with execution continuing immediately after the loop, while *continue* will skip whatever remains of the loop body, and continue execution directly with the next loop iteration.

Modify and extend *execute* to generate code for the extended language. Note that *execute* will need (an) extra argument(s) for contextual information to keep track of the current innermost loop. You may assume that using *break* or *continue* outside any loop is a *static* error. Thus your code generator does not need to handle that case. Your answer should include the modified *execute* cases for *if* and *while*, as well as the cases for the two new commands. (9)

Question 3

This question concerns code improvement (optimisation) and internal representations that facilitate analysis and code improvement.

- (a) Explain the code improvement technique *common subexpression elimination*, illustrating with an example. Also discuss when the technique cannot be applied, again illustrating with an example. (6)
- (b) Show how the following program fragment involving a C-like for-loop might be transformed by means of *loop unrolling* in a situation where the loop bound n is *not* statically known:

```
b[0] := a[0];
for (i := 1; i < n; i++) do
    b[i] := b[i-1] + a[i];
```

Also discuss the potential advantages and disadvantages of this transformation. (9)

- (c) Transform the following code fragment into *static single assignment* (SSA) form:

```
a := 0;
b := 1;
i := 2;
while i < n do begin
    c := a + b;
    a := b;
    b := c;
    i := i + 1
end
```

(10)

Appendix A: MiniTriangle Grammars

This appendix contains the grammars for the MiniTriangle lexical, concrete, and abstract syntax. The following typographical conventions are used to distinguish between terminals and non-terminals:

- nonterminals are written like *this*
- terminals are written like `this`
- terminals with *variable spelling* and special symbols are written like this

MiniTriangle Lexical Syntax:

<i>Program</i>	→	(<i>Token</i> <i>Separator</i>)*
<i>Token</i>	→	<i>Keyword</i> <i>Identifier</i> <i>IntegerLiteral</i> <i>Operator</i> , ; : := = () [] <u>eol</u>
<i>Keyword</i>	→	<code>begin</code> <code>const</code> <code>do</code> <code>else</code> <code>end</code> <code>fun</code> <code>if</code> <code>in</code> <code>let</code> <code>out</code> <code>proc</code> <code>then</code> <code>var</code> <code>while</code>
<i>Identifier</i>	→	<i>Letter</i> <i>Identifier Letter</i> <i>Identifier Digit</i> except <i>Keyword</i>
<i>IntegerLiteral</i>	→	<i>Digit</i> <i>IntegerLiteral Digit</i>
<i>Operator</i>	→	<code>^</code> <code>*</code> <code>/</code> <code>+</code> <code>-</code> <code><</code> <code><=</code> <code>=</code> <code>!=</code> <code>>=</code> <code>></code> <code>&&</code> <code> </code> <code>!</code>
<i>Letter</i>	→	<code>A</code> <code>B</code> ... <code>Z</code> <code>a</code> <code>b</code> ... <code>z</code>
<i>Digit</i>	→	<code>0</code> <code>1</code> <code>2</code> <code>3</code> <code>4</code> <code>5</code> <code>6</code> <code>7</code> <code>8</code> <code>9</code>
<i>Separator</i>	→	<i>Comment</i> <u>space</u> <u>eol</u>
<i>Comment</i>	→	// (any character except <u>eol</u>)* <u>eol</u>

MiniTriangle Concrete Syntax:

<i>Program</i>	→	<i>Command</i>
<i>Commands</i>	→	<i>Command</i> <i>Command ; Commands</i>
<i>Command</i>	→	<i>VarExpression := Expression</i> <i>VarExpression (Expressions)</i> <i>if Expression then Command</i> <i>else Command</i> <i>while Expression do Command</i> <i>let Declarations in Command</i> <i>begin Commands end</i>
<i>Expressions</i>	→	ϵ <i>Expressions₁</i>
<i>Expressions₁</i>	→	<i>Expression</i> <i>Expression , Expressions₁</i>
<i>Expression</i>	→	<i>PrimaryExpression</i> <i>Expression BinaryOperator Expression</i>
<i>PrimaryExpression</i>	→	<u><i>IntegerLiteral</i></u> <i>VarExpression</i> <i>UnaryOperator PrimaryExpression</i> <i>VarExpression (Expressions)</i> <i>[Expressions]</i> <i>(Expression)</i>
<i>VarExpression</i>	→	<u><i>Identifier</i></u> <i>VarExpression [Expression]</i>
<i>BinaryOperator</i>	→	\wedge * / + - < <= == != >= > &&
<i>UnaryOperator</i>	→	- !

<i>Declarations</i>	→	<i>Declaration</i> <i>Declaration ; Declarations</i>
<i>Declaration</i>	→	const <u><i>Identifier</i></u> : <i>TypeDenoter</i> = <i>Expression</i> var <u><i>Identifier</i></u> : <i>TypeDenoter</i> var <u><i>Identifier</i></u> : <i>TypeDenoter</i> := <i>Expression</i> fun <u><i>Identifier</i></u> (<i>ArgDecls</i>) : <i>TypeDenoter</i> = <i>Expression</i> proc <u><i>Identifier</i></u> (<i>ArgDecls</i>) <i>Command</i>
<i>ArgDecls</i>	→	ε <i>ArgDecls</i> ₁
<i>ArgDecls</i> ₁	→	<i>ArgDecl</i> <i>ArgDecl</i> , <i>ArgDecls</i> ₁
<i>ArgDecl</i>	→	<u><i>Identifier</i></u> : <i>TypeDenoter</i> in <u><i>Identifier</i></u> : <i>TypeDenoter</i> out <u><i>Identifier</i></u> : <i>TypeDenoter</i> var <u><i>Identifier</i></u> : <i>TypeDenoter</i>
<i>TypeDenoter</i>	→	<u><i>Identifier</i></u> <i>TypeDenoter</i> [<u><i>IntegerLiteral</i></u>]

Note that the productions for *Expression* make the grammar as stated above ambiguous. Operator precedence and associativity for the *binary* operators as defined in the following table are used to disambiguate:

Operator	Precedence	Associativity
^	1	right
* /	2	left
+ -	3	left
< <= == != >= >	4	non
&&	5	left
	6	left

A precedence level of 1 means the highest precedence, 2 means second highest, and so on.

MiniTriangle Abstract Syntax: $\underline{Name} = \underline{Identifier} \cup \underline{Operator}$.

<i>Program</i>	→ <i>Command</i>	Program
<i>Command</i>	→ <i>Expression</i> := <i>Expression</i> <i>Expression</i> (<i>Expression</i> *) begin <i>Command</i> * end if <i>Expression</i> then <i>Command</i> else <i>Command</i> while <i>Expression</i> do <i>Command</i> let <i>Declaration</i> * in <i>Command</i>	CmdAssign CmdCall CmdSeq CmdIf CmdWhile CmdLet
<i>Expression</i>	→ <u><i>IntegerLiteral</i></u> <u><i>Name</i></u> <i>Expression</i> (<i>Expression</i> *) [<i>Expression</i> *] <i>Expression</i> [<i>Expression</i>]	ExpLitInt ExpVar ExpApp ExpAry ExpIx
<i>Declaration</i>	→ const <u><i>Name</i></u> : <i>TypeDenoter</i> = <i>Expression</i> var <u><i>Name</i></u> : <i>TypeDenoter</i> (:= <i>Expression</i> ϵ) fun <u><i>Name</i></u> (<i>ArgDecl</i> *) : <i>TypeDenoter</i> = <i>Expression</i> proc <u><i>Name</i></u> (<i>ArgDecl</i> *) <i>Command</i>	DeclConst DeclVar DeclFun DeclProc
<i>ArgDecl</i>	→ <i>ArgMode</i> <u><i>Name</i></u> : <i>TypeDenoter</i>	ArgDecl
<i>ArgMode</i>	→ ϵ in out var	ByValue ByRefIn ByRefOut ByRefVar
<i>TypeDenoter</i>	→ <u><i>Name</i></u> → <i>TypeDenoter</i> [<u><i>IntegerLiteral</i></u>]	TDBaseType TDArray

Appendix B: Triangle Abstract Machine (TAM) Instructions

Meta variable	Meaning
a	Address: one of the forms specified by table below when part of an instruction, specific stack address when on the stack
b	Boolean value (false = 0 or true = 1)
ca	Code address; address to routine in the code segment
d	Displacement; i.e., offset w.r.t. address in register or on the stack
l	Label name
m, n, p	Integer
x, y, z	Any kind of stack data
x^n	Vector of n items, $n \geq 0$, here any kind

Address form	Description
[SB + d] [SB - d]	Address given by contents of register SB (Stack Base) +/− displacement d
[LB + d] [LB - d]	Address given by contents of register LB (Local Base) +/− displacement d
[ST + d] [ST - d]	Address given by contents of register ST (Stack Top) +/− displacement d

Instruction	Stack effect	Description
<i>Label</i>		
LABEL l	—	Pseudo instruction: symbolic location
<i>Load and store</i>		
LOADL n	$\dots \Rightarrow n, \dots$	Push literal integer n onto stack
LOADCA l	$\dots \Rightarrow \text{addr}(l), \dots$	Push address of label l (code segment) onto stack
LOAD a	$\dots \Rightarrow [a], \dots$	Push contents at address a onto stack
LOADA a	$\dots \Rightarrow a, \dots$	Push address a onto stack
LOADI d	$a, \dots \Rightarrow [a + d], \dots$	Load indirectly; push contents at address $a + d$ onto stack
STORE a	$n, \dots \Rightarrow \dots$	Pop value n from stack and store at address a
STOREI d	$a, n, \dots \Rightarrow \dots$	Store indirectly; store n at address $a + d$

Instruction	Stack effect	Description
<i>Block operations</i>		
LOADLB $m\ n$	$\dots \Rightarrow m^n, \dots$	Push block of n literal integers m onto stack
LOADIB n	$a, \dots \Rightarrow [a + (n - 1)], \dots, [a + 0], \dots$	Load block of size n indirectly
STOREIB n	$a, x^n, \dots \Rightarrow \dots$	Store block of size n indirectly
POP $m\ n$	$x^m, y^n, \dots \Rightarrow x^m, \dots$	Pop n values below top m values
<i>Arithmetic operations</i>		
ADD	$n_2, n_1, \dots \Rightarrow n_1 + n_2, \dots$	Add n_1 and n_2 , replacing n_1 and n_2 with the sum
SUB	$n_2, n_1, \dots \Rightarrow n_1 - n_2, \dots$	Subtract n_2 from n_1 , replacing n_1 and n_2 with the difference
MUL	$n_2, n_1, \dots \Rightarrow n_1 \cdot n_2, \dots$	Multiply n_1 by n_2 , replacing n_1 and n_2 with the product
DIV	$n_2, n_1, \dots \Rightarrow n_1/n_2, \dots$	Divide n_1 by n_2 , replacing n_1 and n_2 with the (integer) quotient
NEG	$n, \dots \Rightarrow -n, \dots$	Negate n , replacing n with the result
<i>Comparison & logical operations</i> (false = 0, true = 1)		
LSS	$n_2, n_1, \dots \Rightarrow n_1 < n_2, \dots$	Check if n_1 is smaller than n_2 , replacing n_1 and n_2 with the Boolean result
EQL	$n_2, n_1, \dots \Rightarrow n_1 = n_2, \dots$	Check if n_1 is equal to n_2 , replacing n_1 and n_2 with the Boolean result
GTR	$n_2, n_1, \dots \Rightarrow n_1 > n_2, \dots$	Check if n_1 is greater than n_2 , replacing n_1 and n_2 with the Boolean result
AND	$b_2, b_1, \dots \Rightarrow b_1 \wedge b_2, \dots$	Logical conjunction of b_1 and b_2 , replacing b_1 and b_2 with the Boolean result
OR	$b_2, b_1, \dots \Rightarrow b_1 \vee b_2, \dots$	Logical disjunction of b_1 and b_2 , replacing b_1 and b_2 with the Boolean result
NOT	$b, \dots \Rightarrow \neg b, \dots$	Logical negation of b , replacing b with the result

Instruction	Stack effect	Description
<i>Control transfer</i>		
JUMP l	—	Jump unconditionally to location identified by label l
JUMPIFZ l	$n, \dots \Rightarrow \dots$	Jump to location identified by label l if $n = 0$ (i.e., n is false)
JUMPIFNZ l	$n, \dots \Rightarrow \dots$	Jump to location identified by label l if $n \neq 0$ (i.e., n is true)
CALL l	$\dots \Rightarrow \text{PC} + 1, \text{LB}, 0, \dots$	Call global subroutine at location l : Activation record set up by pushing static link (0 for global level), dynamic link (value of LB), and return address (PC+1, address of instruction after the call instruction) onto the stack; PC = l ; LB = start of activation record (address of static link)
CALLI	$ca, sl, \dots \Rightarrow \text{PC} + 1, \text{LB}, sl, \dots$	Call subroutine indirectly: address of routine (ca) and static link to use (sl) on top of the stack; activation record and new PC and LB as for CALL
RETURN $m\ n$	$x^m, y^p, ra, olb, sl, y^n, \dots \Rightarrow x^m, \dots$	Return from subroutine, replacing activation record by result, jumping to return address (PC = ra), and restoring the old local base (LB = olb)
<i>Input/Output</i>		
PUTINT	$n, \dots \Rightarrow \dots$	Print n to the terminal as a decimal integer
PUTCHR	$n, \dots \Rightarrow \dots$	Print the character with character code n to the terminal
GETINT	$\dots \Rightarrow n, \dots$	Read decimal integer n from the terminal and push onto the stack
GETCHR	$\dots \Rightarrow n, \dots$	Read character from the terminal and push its character code n onto the stack
<i>TAM Control</i>		
HALT	—	Stop execution and halt the machine