#### **G53CMP: Lecture 15** *Run-Time Organization II*

Henrik Nilsson

University of Nottingham, UK

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## **This Lecture**

**Data Representation:** how to store various kinds of data.

- General issues
- Primitive types
- Record types
- Arrays
- Disjoint unions
- Recursive types

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We need to encode the data to be stored.

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#### **Data Representation: Issues (1)**

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[Note: The discussion concerns *run-time* representation. Any value that is known *statically* potentially need no run-time representation at all.]

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- Dynamically checked language: Every possible value must have a distinct representation.
- (Statically) typed language: Values of the same type must have distinct representations; the same representation may be reused for values of different types.

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- repr('A') = 01000001
- $\operatorname{repr}(65) = 01000001$

Suppose a variable x contains this value 01000001: Should print (x) print 'A' or 65?

- No way to tell the representation of 'A' and 65 apart in a dynamically checked setting.
- In a statically typed setting, the type is used to disambiguate.

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Example: Consider two enumeration types: data Colour = Red | Green data Size = Small | Large It must *always* be the case that  $repr(Red) \neq repr(Green)$  $repr(Small) \neq repr(Large)$ Further, in a dynamically checked setting:  $\{\operatorname{repr}(\operatorname{Red}), \operatorname{repr}(\operatorname{Green})\} \cap \{\operatorname{repr}(\operatorname{Small}), \operatorname{repr}(\operatorname{Large})\}$  $= \emptyset$ 

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# Uniqueness

Comparison of values is facilitated if each value has exactly one representation.

However, not essential. One exception:

• Floating-point representations typically have a separate sign bit. Thus, the representation of +0 is distinct from the representation of -0.

#### **Data Representation: Issues (2)**

- Constant-size representation: The representations of all values of a given type occupy the same amount of space.
- Direct or indirect (via pointer) representation.

Constant-size representation enables compiler to statically plan storage allocation (since type and hence size is known statically).

If not possible/too wasteful: use some form of indirect representation.

 Direct representation: the representation of a value x is the binary representation of x:

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 Indirect representation: x represented by a handle that points to a binary representation of x (on the stack or in the heap):



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  - supports recursive types (like linked lists, trees)
  - facilitates implementation of *parametric polymorphism* (as handles can be uniform)

# **Representing Primitive Types (1)**

Primitive types are often supported directly by the underlying hardware. For example, a 32-bit machine might support:

- addressing of 8-bit bytes and 32-bit words
- 32-bit twos-complement integer arithmetic
- 64-bit floating point operations

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There are also standard encoding conventions, such as the 7-bit ASCII or 8-bit ISO character codes, or the Unicode standard. Adopting such conventions facilitates interoperability and communication.

# **Representing Primitive Types (2)**

On such a 32-bit machine, the following is a possible representation choice:

Туре	Representation	Size
Boolean	0 for false; 1 for true	8-bit byte
Char	ISO Latin 1 encoding	8-bit byte
Integer	twos-complement repr.	32-bit word
Real	floating point repr.	64-bit word

# **Representing Records (1)**

# A record consists of several fields, each of which has an identifier. For example:

```
type Date = record
               y: Integer,
               m: Integer,
               d: Integer
            end;
type Details = record
                   female: Boolean,
                  dob: Date,
                   status: Char
               end;
```

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- Caveat: alignment restrictions. The underlying architecture might require that e.g. word-sized quantities start at a word boundary.
- Relaxing this is possible, but may require extra work; e.g., accessing a word byte by byte (four instructions instead of one).

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- To satisfy alignment requirements of its components, a variable of aggregate type like a record is commonly aligned according to the maximum alignment of its components.
- Padding may be needed between variables/ components to ensure the alignment requirements of each is met.

### **Exercise: Representing Records (1)**

#### Assume:

- 1 word = 4 byte = 32 bit Integers
- 1 byte = 8 bit Boolean and Char
- Integer must be word aligned

What is the alignment and size of the type Date?

type Date = record

- y: Integer,
- m: Integer,
- d: Integer

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```
end;
```

#### **Exercise: Representing Records (2)**

What is the alignment and size of the type Details?

type Details = record
 female: Boolean,
 dob: Date,
 status: Char
end;

Given a variable x : Details, what are the addresses of x.female, x.dob.y, x.dob.m, x.dob.d, x.status relative to addr(x)?

#### **Exercise: Representing Records (3)**

Size of Date is 3 32-bit words, size of Details
is 1 + 3 + 1 = 5 32-bit words:

variable	address	contents	
x.female	addr(x)	1	(true)
x.dob.y	addr(x) + 4	1984	
x.dob.m	addr(x) + 8	7	
x.dob.d	addr(x) + 12	25	
x.status	addr(x) + 16	117	('u')

# **Example: Records in MiniTriangle**

# Consider the following MiniTriangle program and the resulting (unoptimized) TAM code:

let va	rr:	LOADLB	03
	{a : Integer,	LOADL	1
	b : Boolean,	LOADA	[SB + 0]
	c : Integer}	LOADL	1
in		ADD	
r.	b := true	STOREIB	1
		POP	0 3
		ΗΔΤ.Τ	

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MiniTriangle adopts the set view (and HMTC orders fields alphabetically in a record representation).

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- Two cases:
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- When accessing array elements, must ensure indices are within bounds.
- Address of element computed from base address of array, index, and size of elements.

Static array: required storage space and array bounds known at compile time. Consider:

var x : T[n]

- Required storage:  $n \times \operatorname{sizeof}(T)$
- Access of x[i]:
  - Verify that  $0 \le i \le (n-1)$
  - Compute address *a* of desired element:

 $a = \operatorname{addr}(x[0]) + i \times \operatorname{sizeof}(T)$ 

Fetch/store value at address a.

Example: TAM code for a[3] := 7 given
var a: Integer[10] (at [SB + 0])

LOADL	7		LSS	
LOADA	[SB + 0]		JUMPIFNZ	#1
LOADL	3	#O:	CALL	ixerror
LOAD	[ST - 1]	#1:	LOADL	1
LOADL	0		MUL	
LSS			ADD	
JUMPIFNZ	# O		STOREI	0
LOAD	[ST - 1]			

LOADL 10

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  - storage for array proper allocated at runtime
  - index checked by comparing with array bounds stored in the handle.

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- The value of the tag determines the type of the variant part.
- Mathematically:  $T = T_1 + \ldots + T_n$ ; given tag *i*, the variant part is a value chosen from type  $T_i$ .
- Disjoint unions occur as
  - variant records in Pascal and Ada
  - algebraic data types in Haskell and ML
  - object types in OO languages like Java, C#

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- If constant size is necessary, size is the maximal size over the various possible layouts.

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  - The second tag is JustInt; the variant part is a single integer field.

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- = Triangle Point Point Point
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  - three tags; no variant parts.
  - this is thus just an enumeration type.
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- A recursive type is one defined in terms of itself.
- Examples are linked lists and trees.
- Recursive types are usually represented indirectly since this allows values of arbitrary size to be referenced through a fixed size handle.

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id x = x

can be compiled to a single piece of code working for values of *any* type because all values are represented same way.

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 Recursive types supported automatically: "everything is already a pointer".

- Many OO languages, like Java and C#, adopt a mostly uniform representation:
  - All objects are represented by pointers.
  - Recursive types thus supported.
  - OO-style polymorphism: an object of a class is also an object of any of the superclasses.
  - Uniform layout of "common part" of object to allow superclass methods to work on subclass objects.

# **Example: Haskell Tree Type (1)**

#### This example illustrates

- disjoint union representation
- recursive type representation
- uniform representation (through pointers) of values of all types.

#### **Example: Haskell Tree Type (2)**

data Tree = Leaf Int | Node Tree Tree

aTree = Node (Leaf 1) (Node (Leaf 2) (Leaf 3))

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# **Example: Haskell Tree Type (3)**



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# **Example: Haskell Tree Type (4)**

address	
10200008	
1020000C	
10200010	
10200014	
2E4D0100	
2E4D0104	1
2E4D0108	
2E4D010C	1

coments	
INT	
1	
INT	
2	
Leaf	
10200010	
Leaf	
10200018	

address	contents
2E4D0200	Node
2E4D0204	2E4D0100
2E4D0208	2E4D0108
2E4D020C	Leaf
2E4D0210	10200008
2E4D0214	Node
2E4D0218	2E4D020C
2E4D021C	2E4D0200
•••	

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A pointer to an information table.